

P-Channel 12 V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)	Q _g (Typ.)
- 12	0.156 at V _{GS} = - 4.5 V	1.18	6.7 nC
	0.190 at V _{GS} = - 2.5V	1.07	
	0.245 at V _{GS} = - 1.8V	0.49	

FEATURES

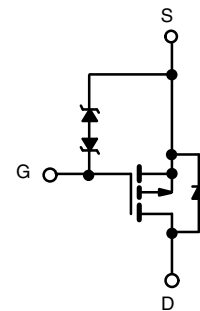
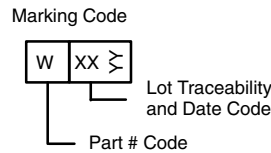
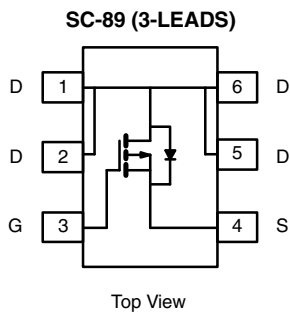
- TrenchFET® Power MOSFET
- 100 % R_g Tested
- Material categorization:
For definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Load Switch for Portable Devices



Ordering Information: Si1065X-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	- 12	V
Gate-Source Voltage	V _{GS}	± 8	
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	- 1.18 ^{b, c}	A
	T _A = 70 °C	- 0.94 ^{b, c}	
Pulsed Drain Current	I _{DM}	- 8	
Continuous Source-Drain Diode Current	I _S	- 0.2 ^{b, c}	W
Maximum Power Dissipation ^a	T _A = 25 °C	0.236 ^{b, c}	
	T _A = 70 °C	0.151 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{a, b}	R _{thJA}	440	530	°C/W	
		Steady State State	540		

Notes:

- Maximum under steady state conditions is 650 °C/W.
- Surface mounted on 1" x 1" FR4 board.
- t = 5 s.

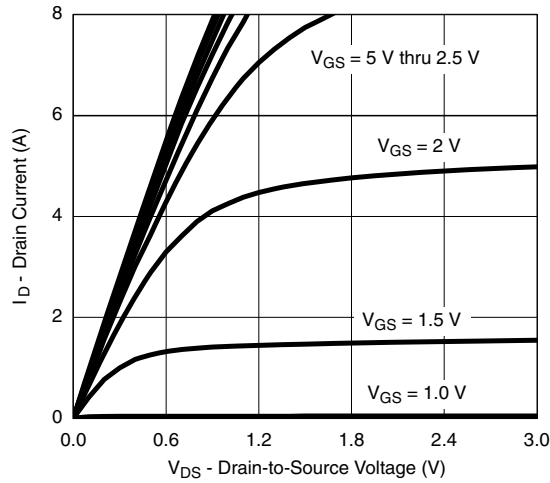
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 12			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = - 250 μA		- 8.47		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			2.33		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 0.45		- 0.95	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 8 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 12 V, V _{GS} = 0 V			- 1	nA
		V _{DS} = - 12 V, V _{GS} = 0 V, T _J = 85 °C			- 10	μA
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = - 4.5 V	- 8			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 1.18 A		0.108	0.156	Ω
		V _{GS} = - 2.5 V, I _D = - 1.07 A		0.131	0.190	
		V _{GS} = - 1.8 V, I _D = - 0.49 A		0.158	0.245	
Forward Transconductance	g _{fs}	V _{DS} = - 6 V, I _D = - 1.18 A		5.18		S
Dynamic^b						
Input Capacitance	C _{iss}	V _{DS} = - 6 V, V _{GS} = 0 V, f = 1 MHz		480		pF
Output Capacitance	C _{oss}			190		
Reverse Transfer Capacitance	C _{rss}			145		
Total Gate Charge	Q _g	V _{DS} = - 6 V, V _{GS} = - 5 V, I _D = - 1.18 A		7.2	10.8	nC
				6.7	10.1	
Gate-Source Charge	Q _{gs}	V _{DS} = - 6 V, V _{GS} = - 4.5 V, I _D = - 1.18		0.84		
Gate-Drain Charge	Q _{gd}			2.7		
Gate Resistance	R _g	f = 1 MHz		10	15	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 6 V, R _L = 6.32 Ω I _D ≅ - 0.95 A, V _{GEN} = - 4.5 V, R _g = 1 Ω		13	19.5	ns
Rise Time	t _r			27	40.5	
Turn-Off Delay Time	t _{d(off)}			45	67.5	
Fall Time	t _f			27	40.5	
Drain-Source Body Diode Characteristics						
Pulse Diode Forward Current ^a	I _{SM}				8	A
Body Diode Voltage	V _{SD}	I _S = - 0.63 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = - 0.7 A, di/dt = 100 A/μs		29.2	44	nC
Body Diode Reverse Recovery Charge	Q _{rr}			10.22	15.3	ns
Reverse Recovery Fall Time	t _a			13.7		
Reverse Recovery Rise Time	t _b			15.5		

Notes:

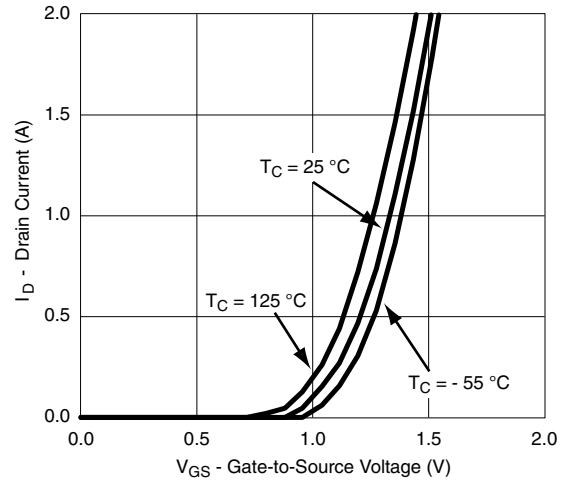
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

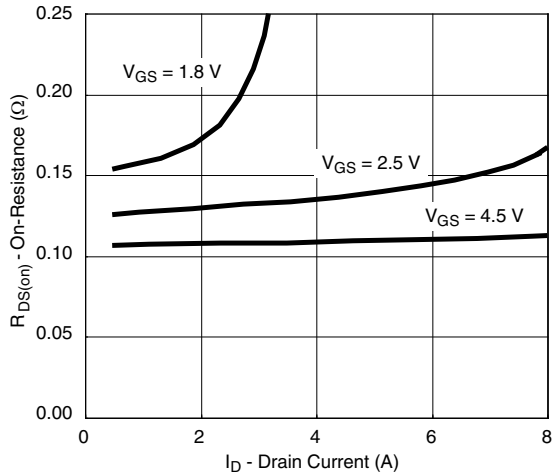
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



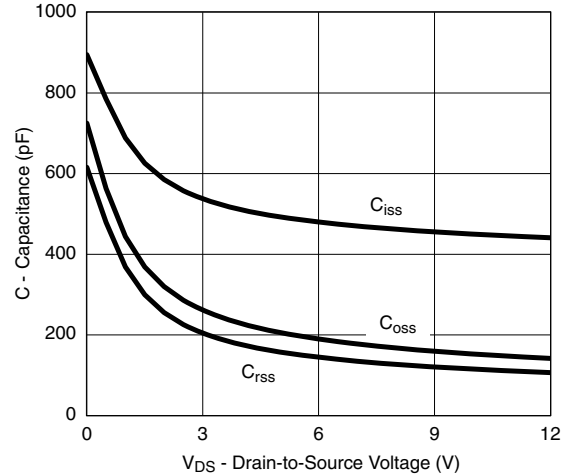
Output Characteristics



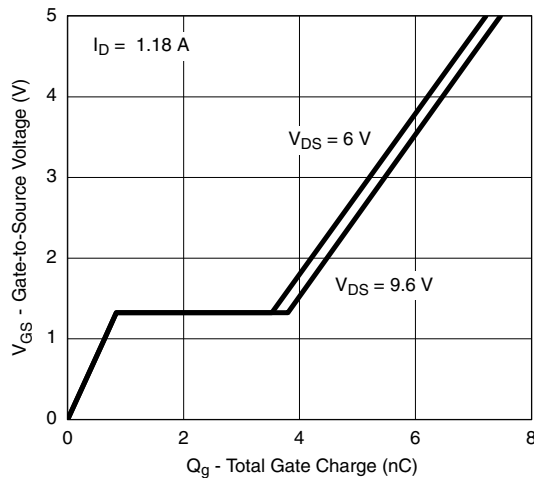
Transfer Characteristics Curves vs. Temp.



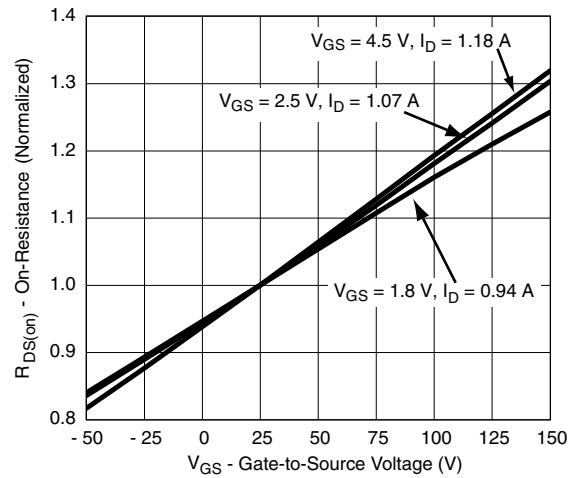
On-Resistance vs. Drain Current



Capacitance

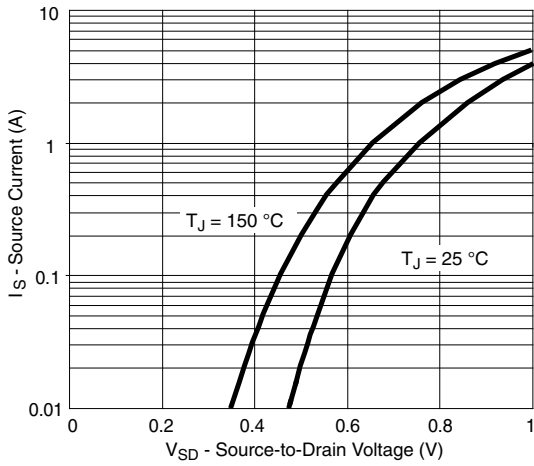


Gate Charge

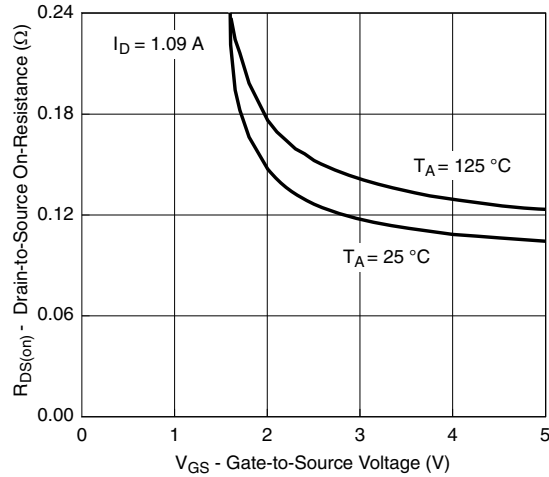


On-Resistance vs. Junction Temperature

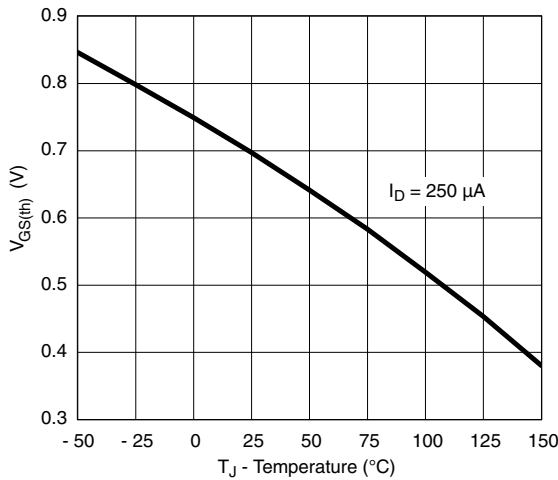
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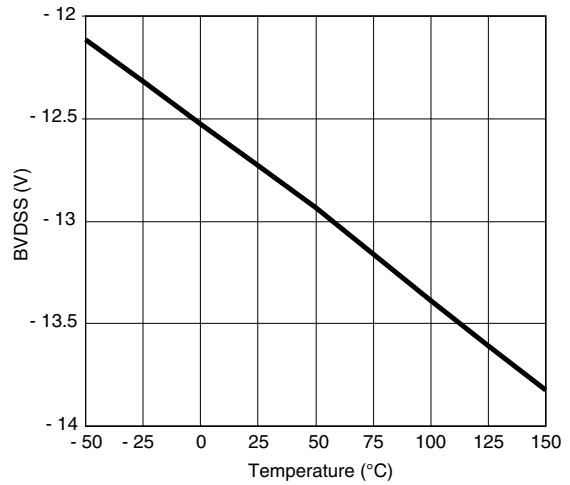
Source-Drain Diode Forward Voltage



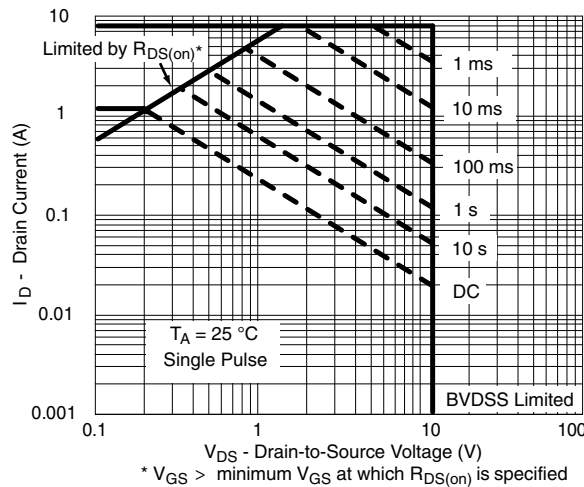
$R_{DS(on)}$ vs. V_{GS} vs. Temperature



Threshold Voltage

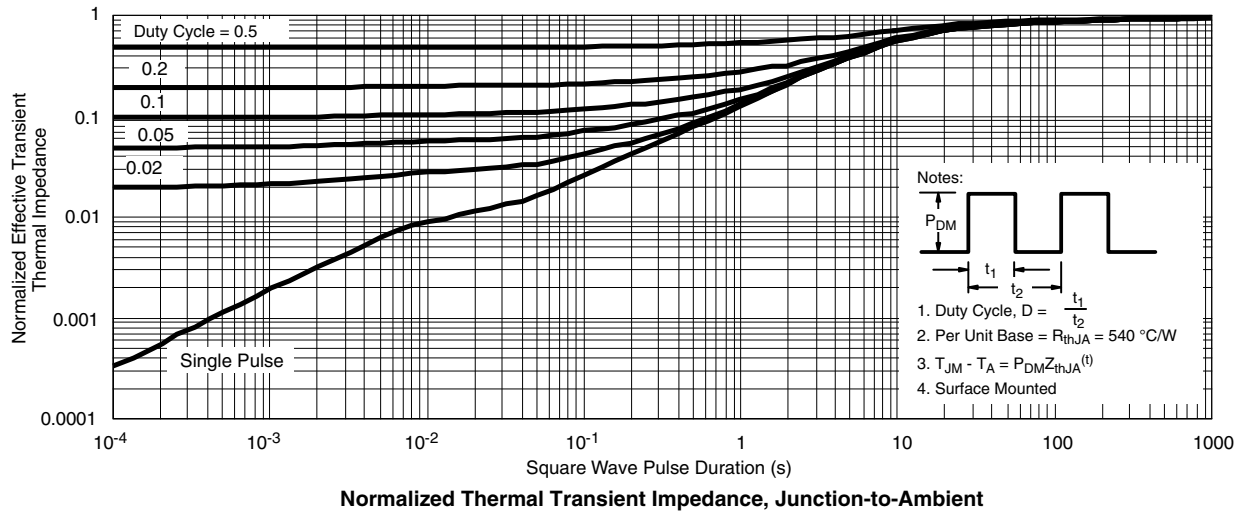


BVDSS vs. Temperature



Safe Operating Area, Junction-to-Ambient

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



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SC-89 6-Leads (SOT-563F)



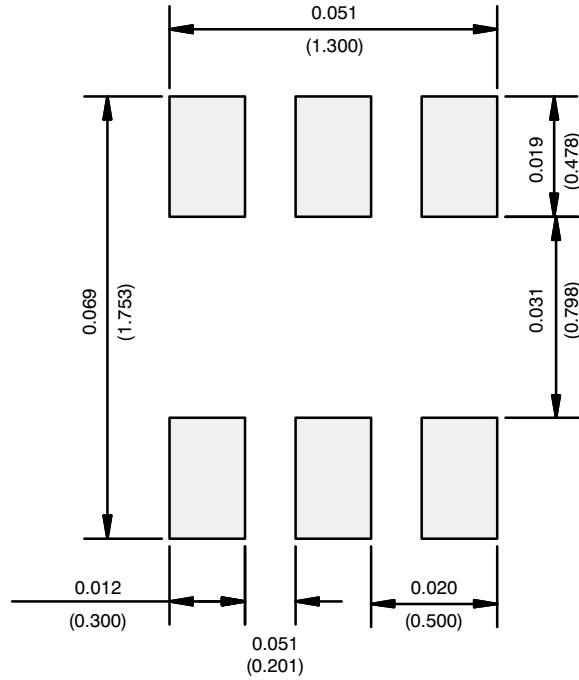
Notes

- 1. Dimensions in millimeters.
- ⚠ Dimension D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.
- ⚠ Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.
- ⚠ Datums A, B and D to be determined 0.10 mm from the lead tip.
- ⚠ Terminal numbers are shown for reference only.
- ⚠ These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.56	0.58	0.60
A1	0	0.02	0.10
b	0.15	0.22	0.30
c	0.10	0.14	0.18
D	1.50	1.60	1.70
E	1.50	1.60	1.70
E1	1.15	1.20	1.25
e	0.45	0.50	0.55
e1	0.95	1.00	1.05
L	0.25	0.35	0.50
L1	0.10	0.20	0.30

C14-0439-Rev. C, 11-Aug-14
DWG: 5880

RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads
Dimensions in Inches/(mm)

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